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|-----------------------------------|---------------------------------------|--|-------------|
| <b>Notice of References Cited</b> | Application/Control No.<br>09/925,161 | Applicant(s)/Patent Under<br>Reexamination<br>PURCELL ET AL. |             |
|                                   | Examiner<br>Fred Ferris               | Art Unit<br>2128   | Page 1 of 1 |

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name                | Classification |
|---|---|--|-----------------|---------------------|----------------|
|   | A | US-6,535,901 B1                                  | 03-2003         | Grisamore, Robert T | 708/629        |
|   | B | US-6,571,268 B1                                  | 05-2003         | Giacalone et al.    | 708/524        |
|   | C | US-2002/0039386 A1                               | 04-2002         | Han et al.          | 375/240.16     |
|   | D | US-5,937,202 A                                   | 08-1999         | Crosetto, Dario B.  | 712/19         |
|   | E | US-5,666,116                                     | 09-1997         | Bakhmutsky, Michael | 341/67         |
|   | F | US-  |                 |                     |                |
|   | G | US-  |                 |                     |                |
|   | H | US-  |                 |                     |                |
|   | I | US-  |                 |                     |                |
|   | J | US-  |                 |                     |                |
|   | K | US-  |                 |                     |                |
|   | L | US-  |                 |                     |                |
|   | M | US-  |                 |                     |                |

**FOREIGN PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
|   | N |  |                 |         |      |                |
|   | O |  |                 |         |      |                |
|   | P |  |                 |         |      |                |
|   | Q |  |                 |         |      |                |
|   | R |  |                 |         |      |                |
|   | S |  |                 |         |      |                |
|   | T |  |                 |         |      |                |

**NON-PATENT DOCUMENTS**

| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)  |
|---|---|--|
|   | U | "Accelerating Pipelined Integer and Floating-Point Accumulations in Configurable Hardware with Delayed Addition Techniques", Luo et al, IEEE Transactions on Computers, Vol. 49, No. 3, March 2000 |
|   | V | "Architectures for Pipelined Wallace Tree Multiplier-Accumulators", Pang, IEEE CH2909-0/90/0000/0247, IEEE 1990  |
|   | W |  |
|   | X |  |

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.